

FIG. 1  
Related Art

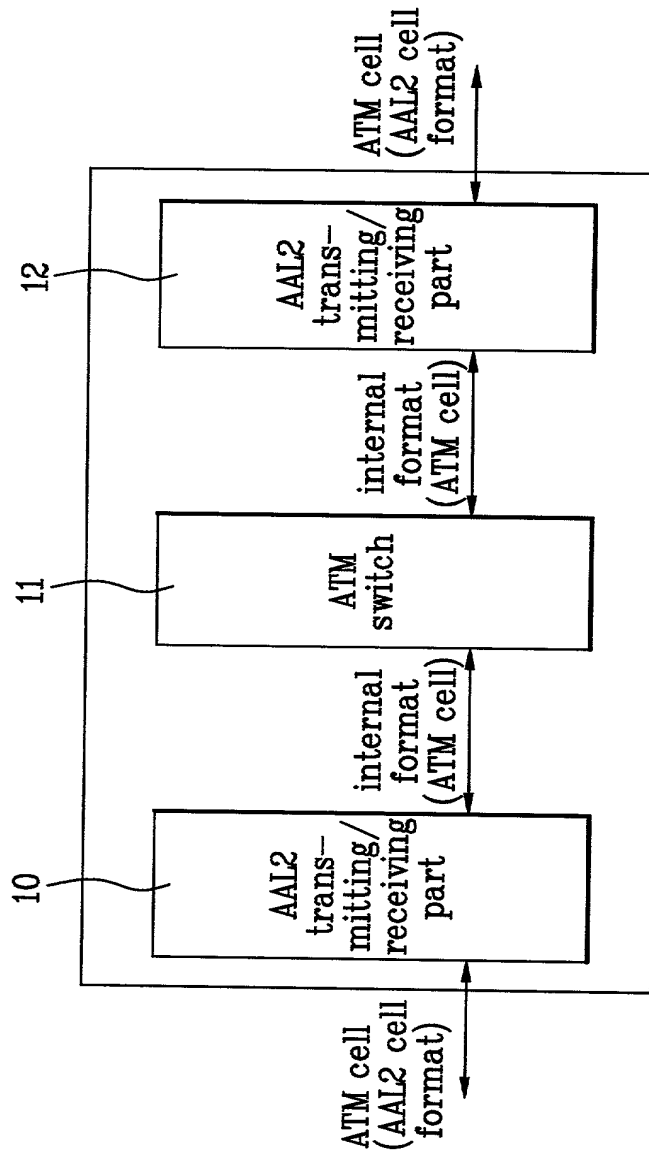


FIG. 2  
Related Art

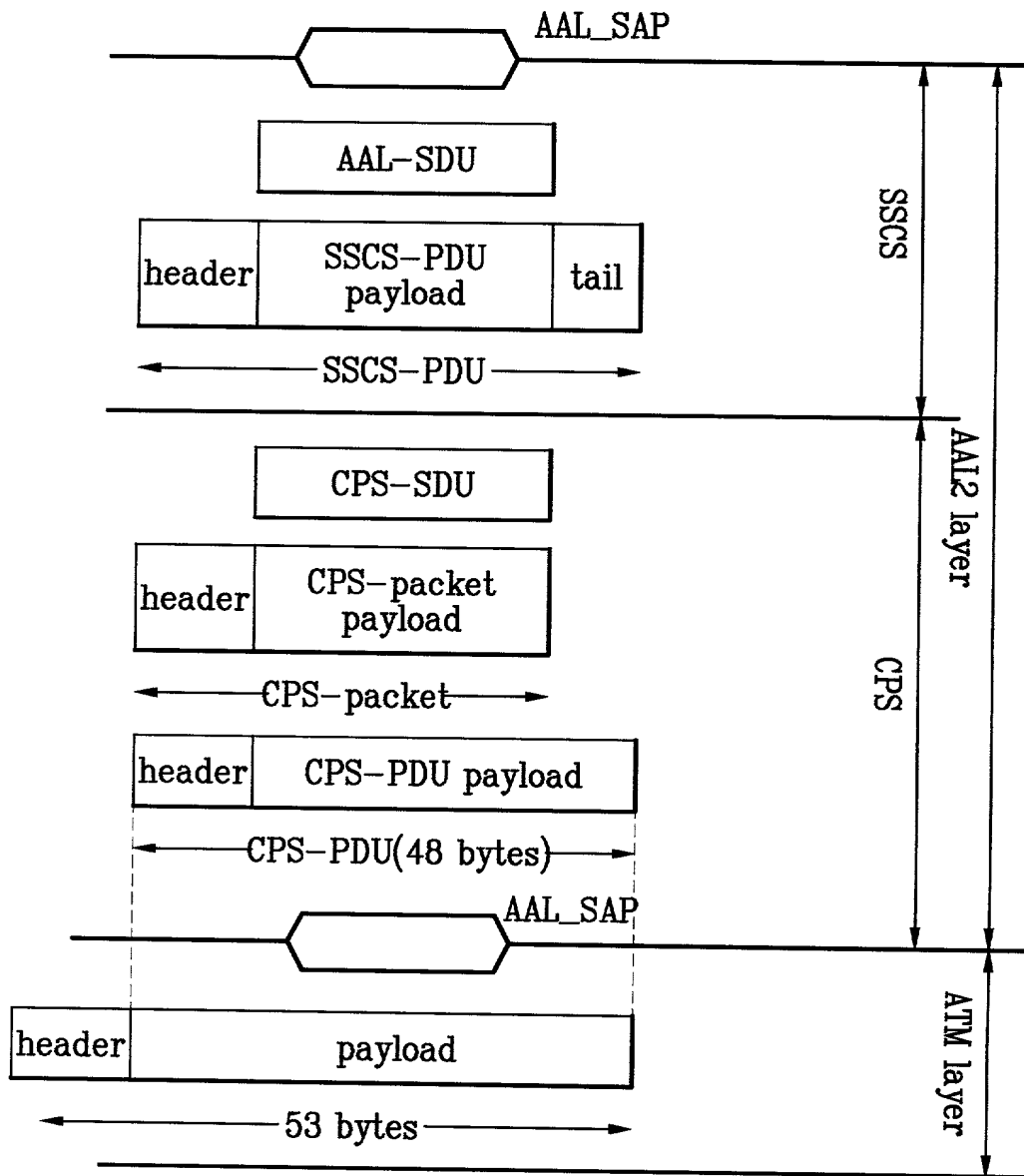


FIG. 3

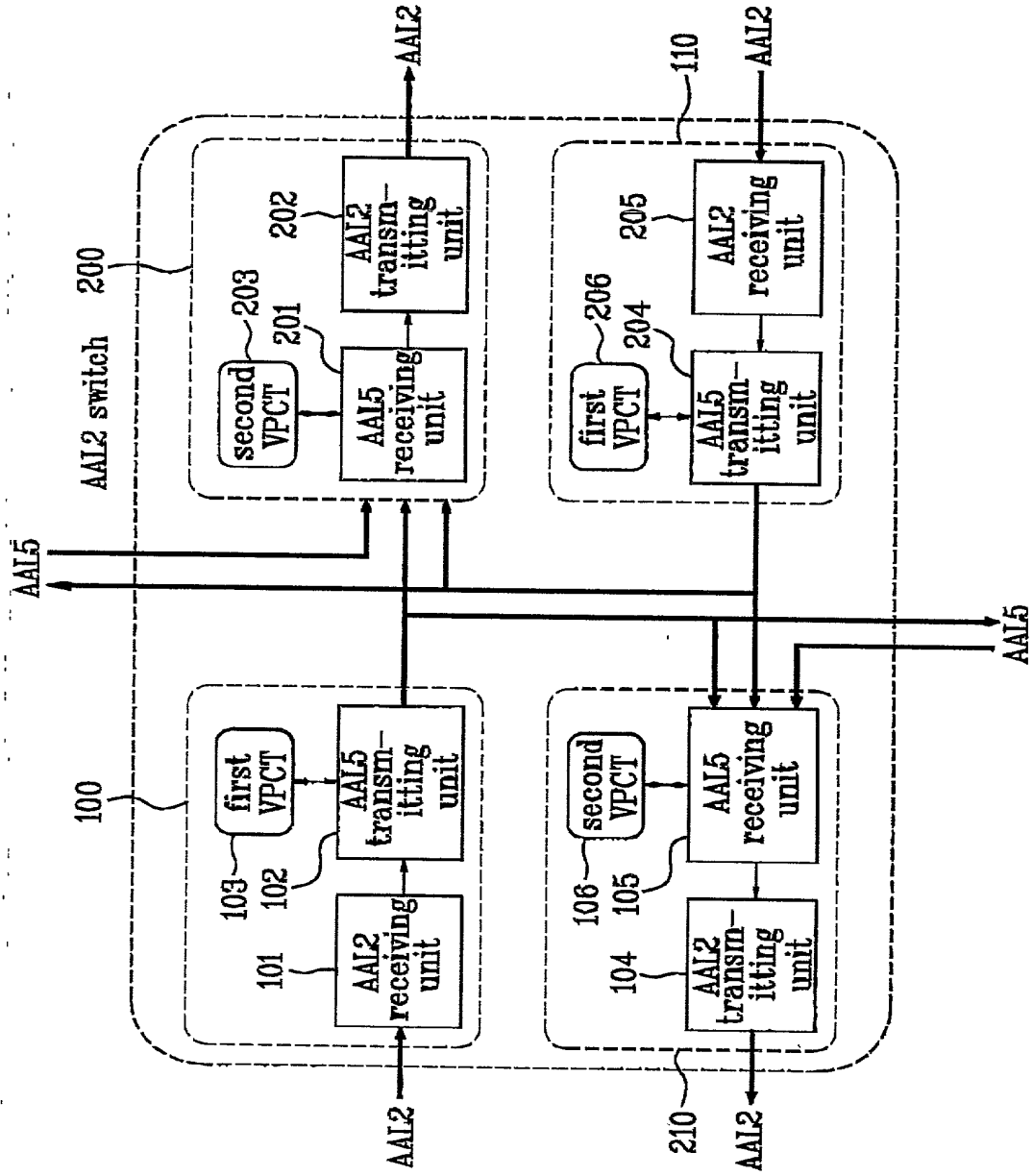


FIG. 4A

FIRST VPCT

input	output
VP/VC & CID #1	New VP/VC #1
VP/VC & CID #2	New VP/VC #2
VP/VC & CID #3	New VP/VC #3
⋮	⋮
VP/VC & CID #N-1	New VP/VC #N-1
VP/VC & CID #N	New VP/VC #N

FIG. 4B

SECOND VPCT

input	output
VP/VC #1	NEW VP/VC & CID #1
VP/VC #2	NEW VP/VC & CID #2
VP/VC #3	NEW VP/VC & CID #3
⋮	⋮
VP/VC #N-1	NEW VP/VC & CID #N-1
VP/VC #N	NEW VPNC & CID #N

FIG. 5

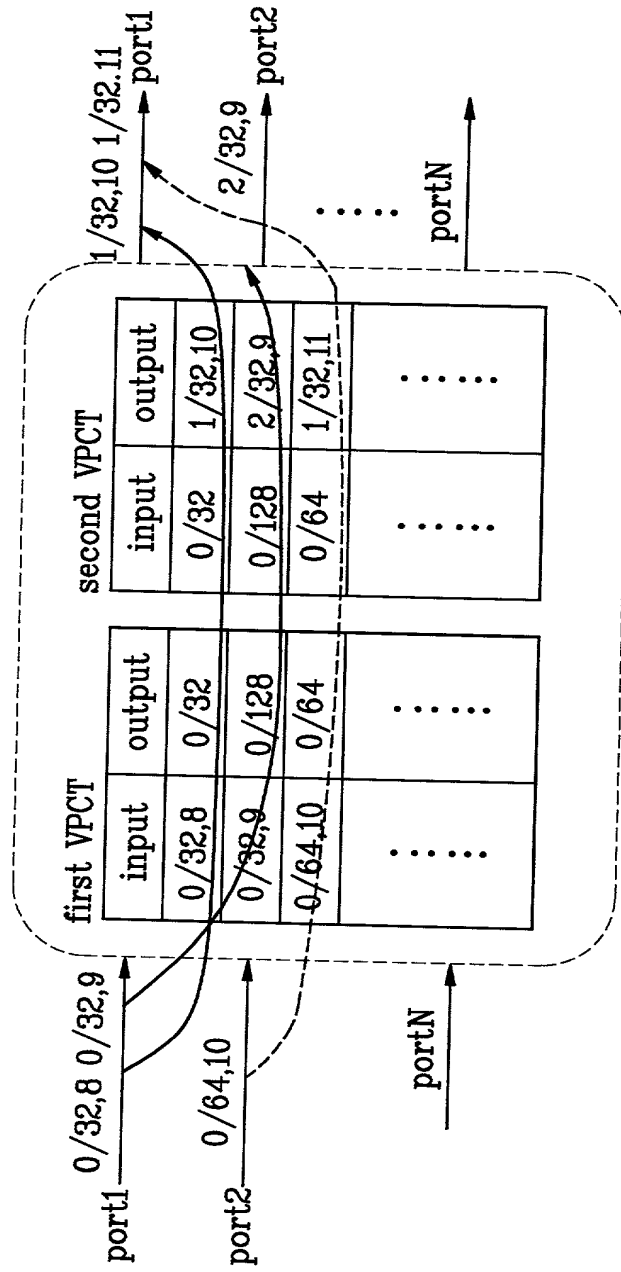


FIG. 6

